

100V, 80A N-CHANNEL POWER MOSFET

GENERAL DESCRIPTION

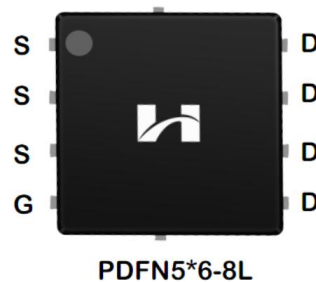
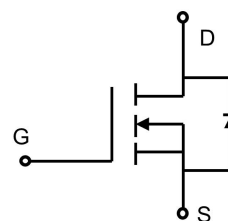
The SFM10008T uses advanced SGT technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety applications.

Features

- ◆ $V_{DS}=100V, I_D=80A$
- ◆ $R_{DS(on)}$
TYP: $6.8m\Omega @ V_{GS}=10V$

Applications

- ◆ Power faction correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
SFM10008T	PDFN5*6-8L	SFM10008T	Pb Free	Reel

ABSOLUTE MAXIMUM RATINGS ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Characteristics	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current	I_D	$T_C = 25^{\circ}\text{C}$	A
		$T_C = 100^{\circ}\text{C}$	
Drain Current Pulsed(Note 1)	I_{DM}	320	A
Power Dissipation($T_C=25^{\circ}\text{C}$) -Derate above 25°C	P_D	98	W
Single Pulsed Avalanche Energy (Note 2)	E_{AS}	350	mJ
Operation Junction Temperature Range	T_J	$-55\sim+150$	$^{\circ}\text{C}$
Storage Temperature Range	T_{stg}	$-55\sim+150$	$^{\circ}\text{C}$
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	TL	300	$^{\circ}\text{C}$

THERMAL CHARACTERISTICS

Characteristics	Symbol	MAX	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.85	$^{\circ}\text{C/W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	52	$^{\circ}\text{C/W}$

ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
Drain -Source Breakdown Voltage	B _{VDSS}	V _{GS} =0V, I _D =250μA	100	--	--	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V	--	--	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =20V, V _{DS} =0V	--	--	100	nA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =-20V, V _{DS} =0V	--	--	-100	
On Characteristics						
Gate Threshold Voltage	V _{GS(th)}	V _{GS} = V _{DS} , I _D =250μA	1.4	1.8	2.4	V
Static Drain- Source On State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A	--	6.8	8.5	mΩ
Dynamic Characteristics						
Gate Resistance	R _g	V _{GS} =0V, f=1.0MHZ	1	2.1	10	Ω
Input Capacitance	C _{iSS}	V _{DS} =50V V _{GS} =0V f=1.0MHZ	--	3960	--	pF
Output Capacitance	C _{oSS}		--	425	--	
Reverse Transfer Capacitance	C _{rSS}		--	11.5	--	
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}	V _{DD} =50V, V _{GS} =10V R _G =3Ω, I _D =20A (Note 3.4)	--	18.5	--	ns
Turn-on Rise Time	t _r		--	29.3	--	
Turn-off Delay Time	t _{d(off)}		--	62.5	--	
Turn-off Fall Time	t _f		--	16.1	--	

Total Gate Charge	Q_g	$V_{DD}=50V, I_D=20A$ $V_{GS}=10V$ (Note 3.4)	--	63.4	--	nc
Gate-Source Charge	Q_{gs}		--	17.5	--	
Gate-Drain Charge	Q_{gd}		--	4.3	--	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	I_S	Integral Reverse P-N Junction Diode in the MOSFET	--	--	80	A
Pulsed Source Current	I_{SM}		--	--	320	
Diode Forward Voltage	V_{SD}	$I_S=20A, V_{GS}=0V$	--	0.82	1.2	V
Reverse Recovery Time	T_{rr}	$I_F=20A$ $dI_F/dt=100A/\mu S$	--	85	--	ns
Reverse Recovery Charge	Q_{rr}		--	0.15	--	μC

- 1.Pulse width limited by maximum junction temperature
2. $L=0.5mH, V_{DD}=50V, V_G=10V, R_G=25\Omega$, starting $T_J=25^\circ C$
- 3.Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
- 4.Essentially independent of operating temperature

Typical Performance Characteristics

Figure 1. On-Region Characteristics

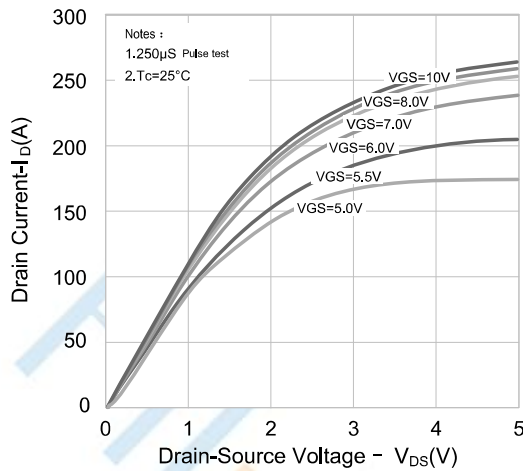


Figure 2. Transfer Characteristics

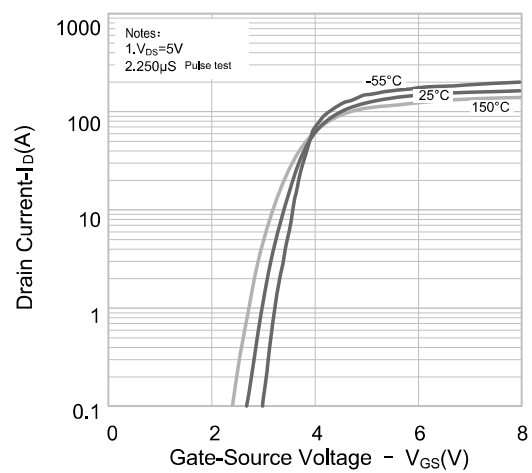


Figure 3. On-Resistance Variation vs. Drain-Current, Gate Voltage

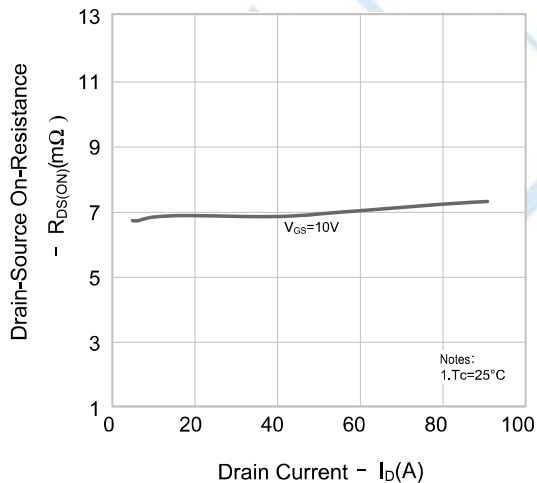


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

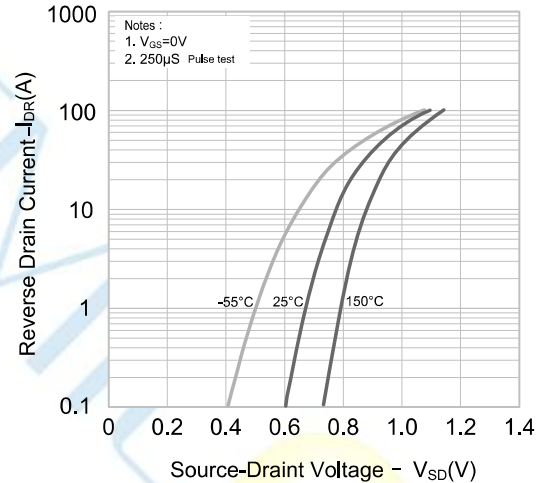


Figure 5. Capacitance Characteristics

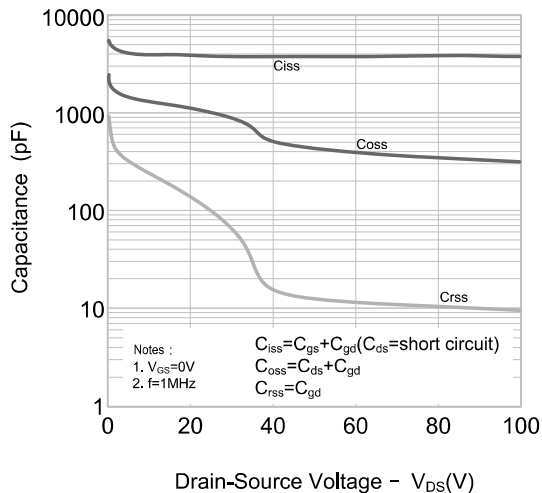
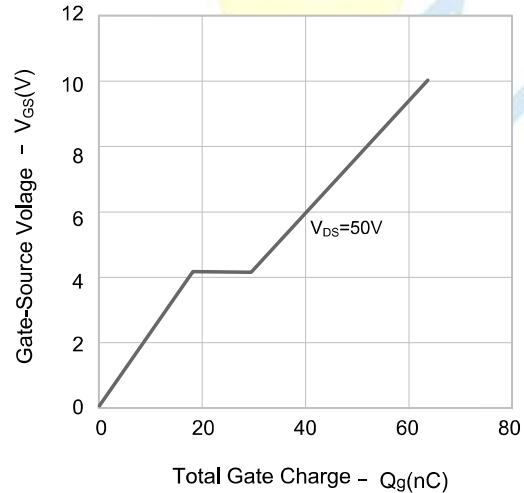


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics

Figure 7. Breakdown Voltage Variation vs. Temperature

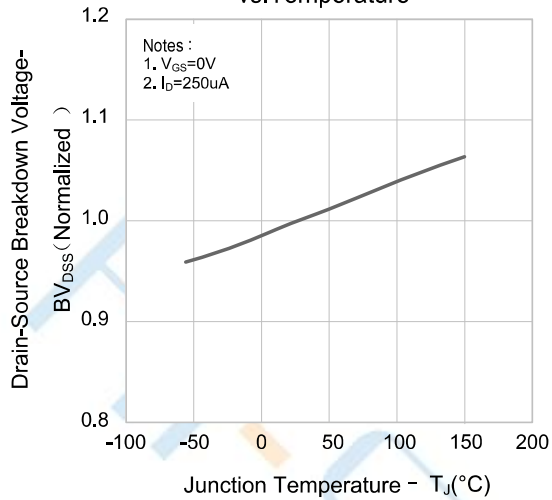


Figure 8. On-resistance Variation vs. Temperature

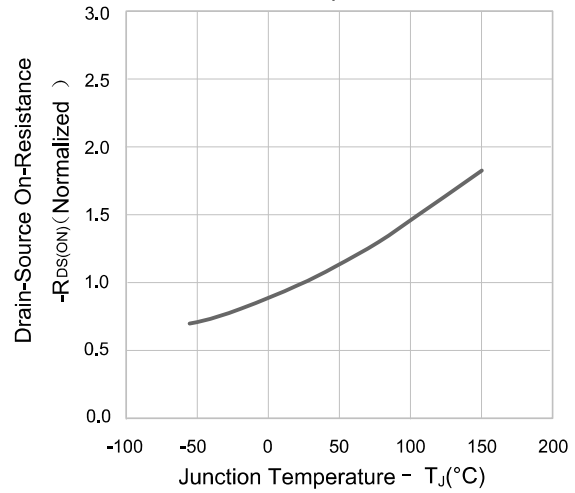
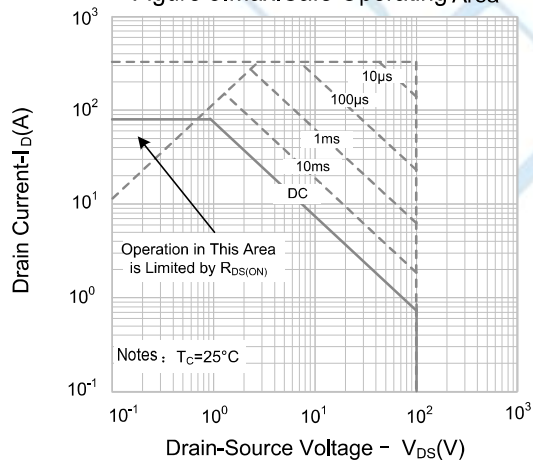
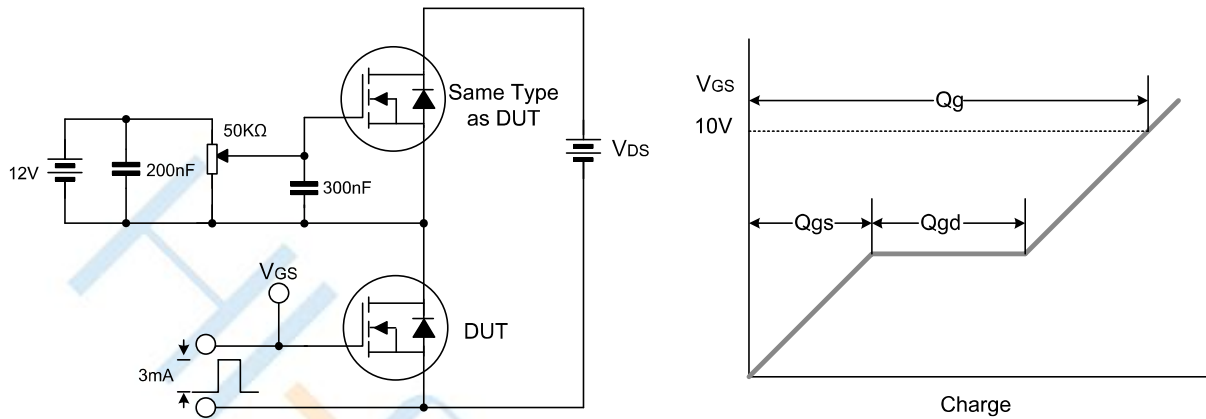


Figure 9. Max. Safe Operating Area

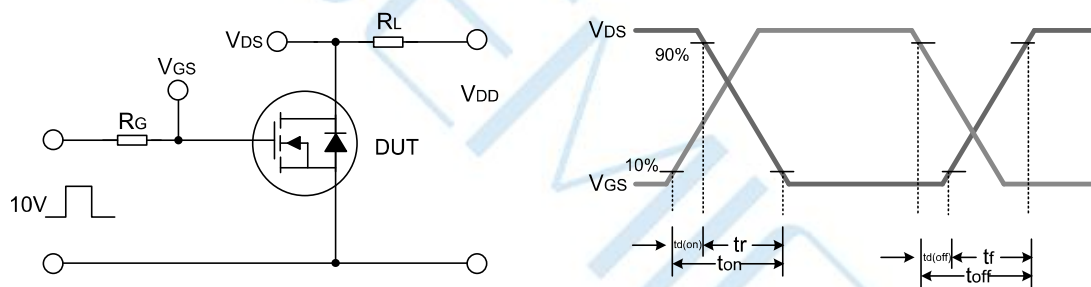


Test Circuit

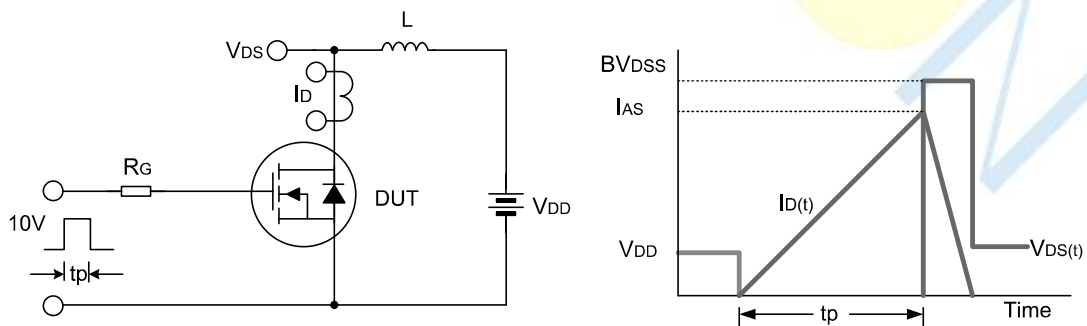
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform

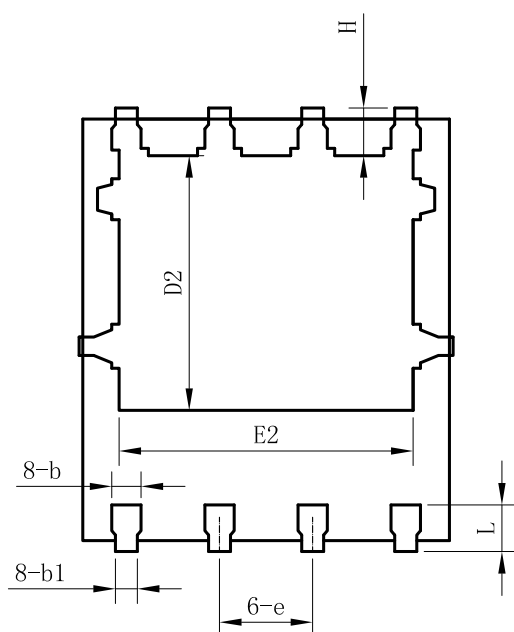
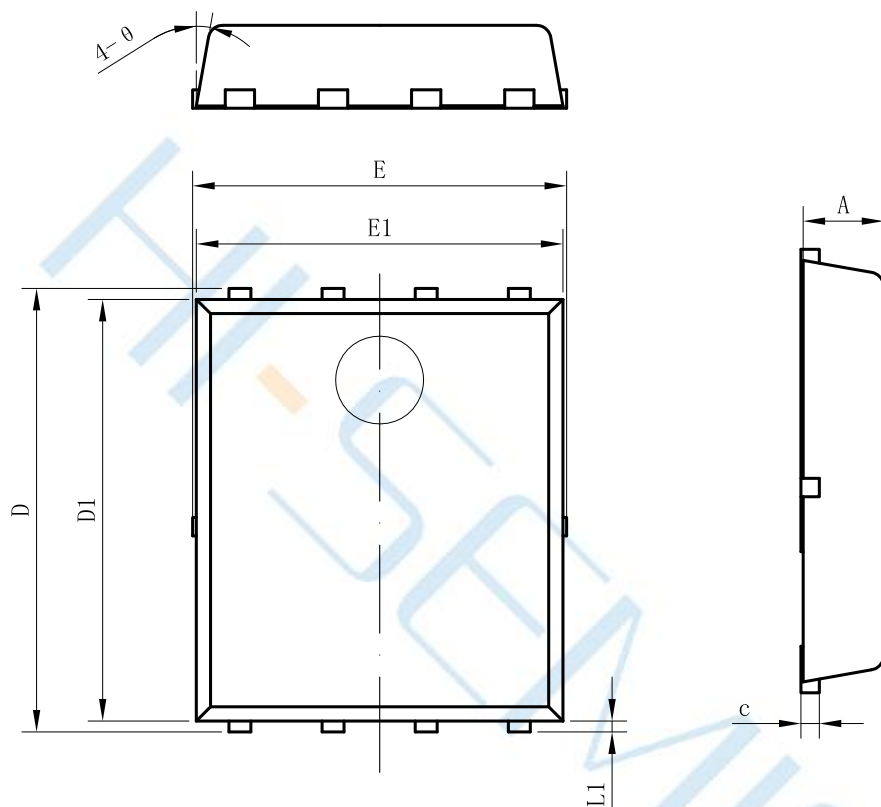


Unclamped Inductive Switching Test Circuit & Waveform



Package Dimensions of PDFN5*6-8L

Unit:mm



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	1.0	1.1	1.2
b	0.35	0.4	0.45
b1		(0.3)	
c	0.2	0.25	0.35
D	5.9	6.05	6.2
D1	5.65	5.75	5.85
D2		(3.475)	
E			5.2
E1	4.9	5	5.1
E2		(4.01)	
e		1.27BSC	
H	0.5	0.65	0.75
L	0.51	0.635	0.75
L1		0.15	
θ		10°	

Disclaimer:

- ▶ Hi-semicon reserves the right to make changes to the information herein for the improvement of the design and performance without further notice! Customers should obtain the latest relevant information before placing orders and should verify that such information is complete and current.
- ▶ All semiconductor products malfunction or fail with some probability under special conditions. When using Hi-semicon products in system design or complete machine manufacturing, it is the responsibility of the buyer to comply with the safety standards strictly and take essential measures to avoid situations in which a malfunction